

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: <b>Davidow</b>	Confirmation No.: <b>6677</b>
Application No.: <b>10/661,034</b>	Group Art Unit: <b>2836</b>
Filing Date: <b>September 12, 2003</b>	Examiner: <b>Jackson, Stephen W</b>
For: <b>Method Voltage Signal Coupling Structure For Last Leg Power Grid High-Speed Data Network</b>	

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

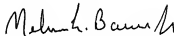
In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2), which are submitted herewith.

Please charge any deficiency or credit any overpayment to Deposit Account No. 50-3970.

Respectfully submitted,



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Date: December 15, 2006

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